

FIG. 1

FIG. 2 is a block diagram of a protocol processor element 200.

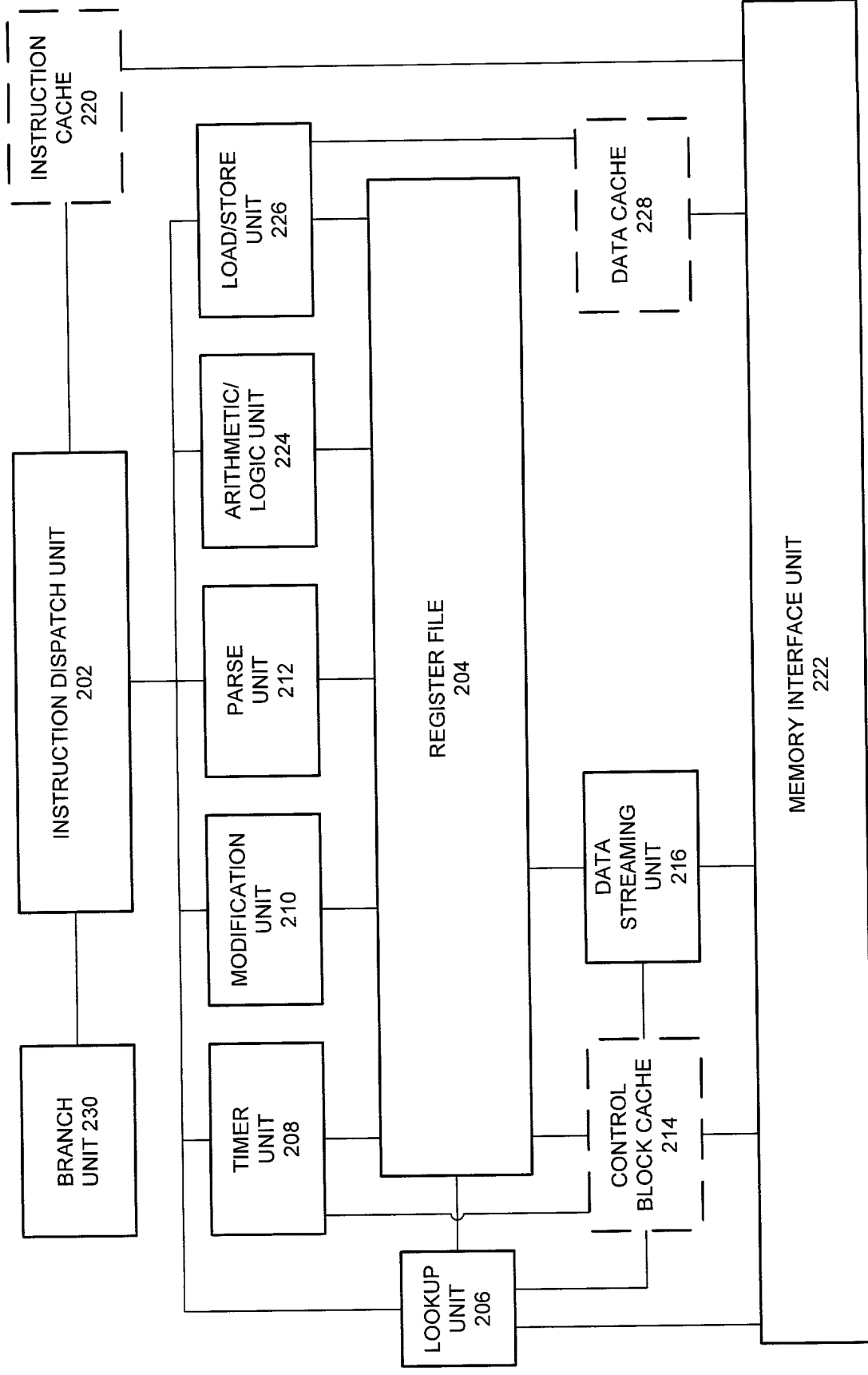


FIG. 2

FIG. 2

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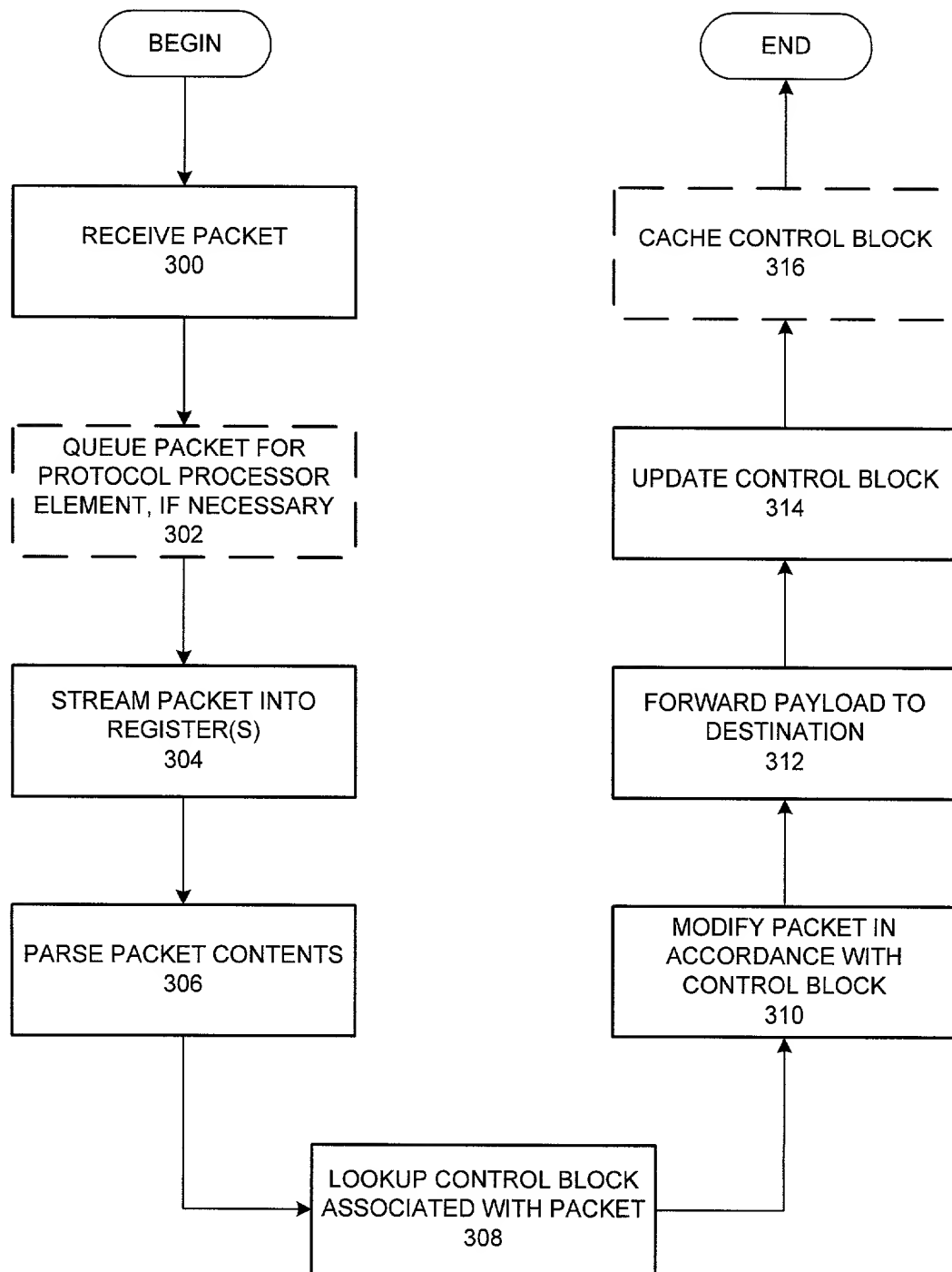


FIG. 3

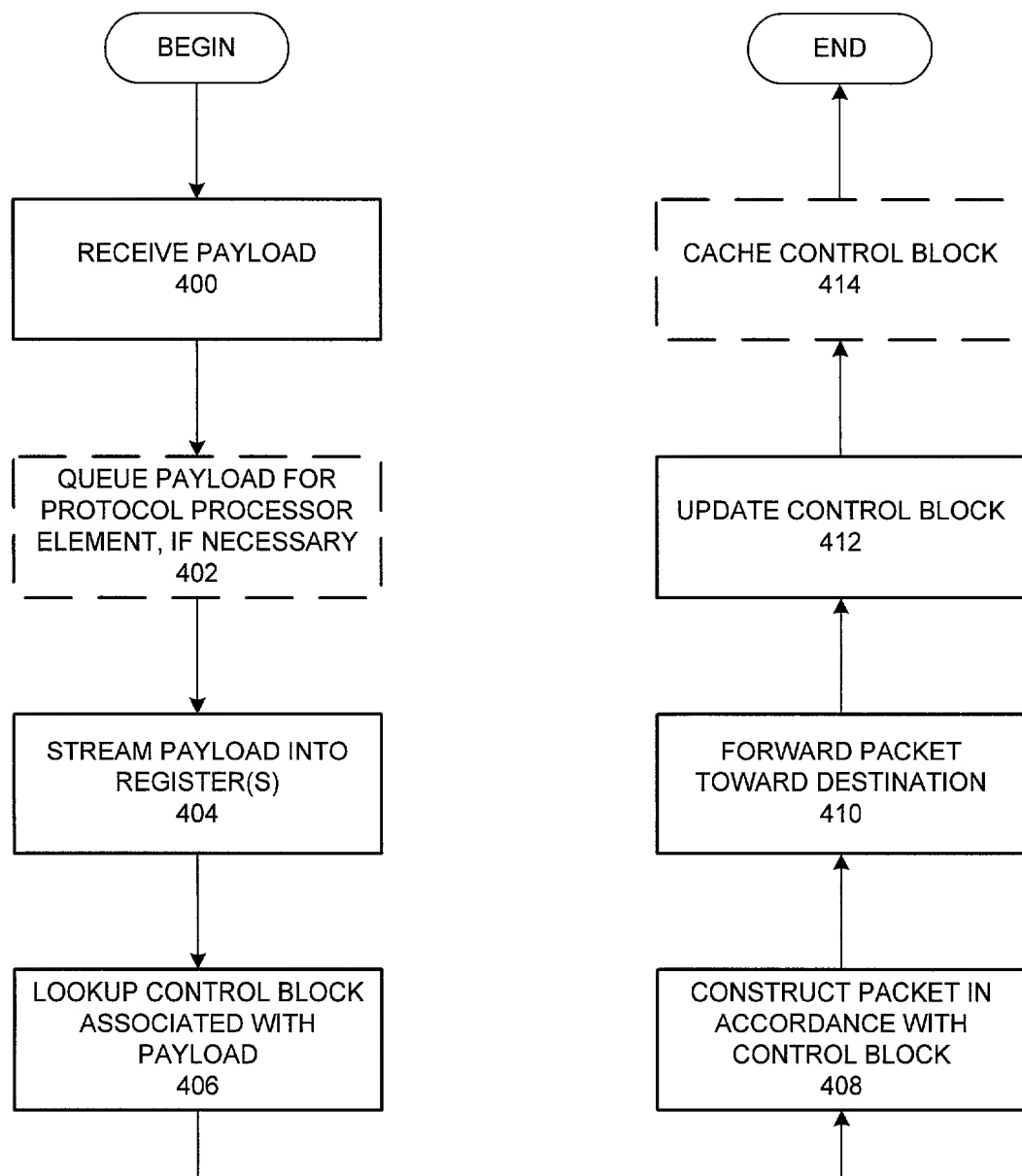


FIG. 4